

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)	:	Stefan Scholl	)	I hereby certify that this paper is being deposited with the United States Postal			
Ser. No.	:	10/668,684	)	Service as First Class Mail, postage prepaid, in an envelope addressed to:			
Filed	:	September 23, 2003	)	MS Missing Parts Commissioner for Patents			
Title	:	Process For Designing And Manufacturing Semi-Conductor Memory Components, In Particular DRAM Components	) ) )	P.O. Box 1450 Alexandria, VA 22313-1450, on this date: Date: February 17, 2004			
Art Unit	:	2825	)	Paul C. Craane Registration No. 38,851			
Examiner	:	Not Yet Assigned	)	· ·			

## INFORMATION DISCLOSURE STATEMENT

MS Missing Parts Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. §1.56, the documents identified on the enclosed PTO-1449 form, copies of which are enclosed, are disclosed to the Examiner for consideration in connection with the present application. The submission of the documents identified on the enclosed PTO-1449 form is not an admission that they are prior art or material to patentability as defined in 37 C.F.R. §1.56(b).

This Information Disclosure Statement is filed before the mailing date of the first Office Action on the merits, so no fee is believed to be due. However, the Commissioner is hereby authorized and directed to charge any required fee for the submission of this information disclosure statement to Deposit Account No. 13-2855. A copy of this paper is enclosed.

By:

Respectfully submitted,

MARSHALL, GERSTEIN & BORUN LLP

Date: February 17, 2004

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DATE CONSIDERED:

EXAMINER: